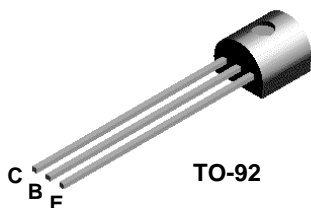
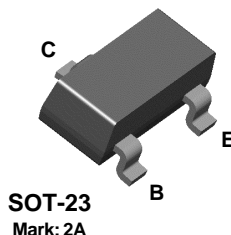


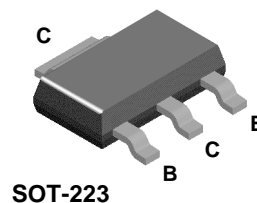
## 2N3906



## MMBT3906



## PZT3906



## PNP General Purpose Amplifier

This device is designed for general purpose amplifier and switching applications at collector currents of 10  $\mu$ A to 100 mA.

### Absolute Maximum Ratings\*

$T_A = 25^\circ\text{C}$  unless otherwise noted

| Symbol         | Parameter  | Value       | Units            |
|----------------|--|-------------|------------------|
| $V_{CEO}$      | Collector-Emitter Voltage                        | 40          | V                |
| $V_{CBO}$      | Collector-Base Voltage                           | 40          | V                |
| $V_{EBO}$      | Emitter-Base Voltage                             | 5.0         | V                |
| $I_C$          | Collector Current - Continuous                   | 200         | mA               |
| $T_J, T_{stg}$ | Operating and Storage Junction Temperature Range | -55 to +150 | $^\circ\text{C}$ |

\* These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

#### NOTES:

- 1) These ratings are based on a maximum junction temperature of 150 degrees C.
- 2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.
- 3) All voltages (V) and currents (A) are negative polarity for PNP transistors.

### Thermal Characteristics

$T_A = 25^\circ\text{C}$  unless otherwise noted

| Symbol          | Characteristic                          | Max    |           |           | Units                |
|-----------------|---|--------|-----------|-----------|----------------------|
|                 |   | 2N3906 | *MMBT3906 | **PZT3906 |                      |
| $P_D$           | Total Device Dissipation                | 625    | 350       | 1,000     | mW                   |
|                 | Derate above 25 $^\circ\text{C}$        | 5.0    | 2.8       | 8.0       | mW/ $^\circ\text{C}$ |
| $R_{\theta JC}$ | Thermal Resistance, Junction to Case    | 83.3   |           |           | $^\circ\text{C/W}$   |
| $R_{\theta JA}$ | Thermal Resistance, Junction to Ambient | 200    | 357       | 125       | $^\circ\text{C/W}$   |

\* Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06."

\*\* Device mounted on FR-4 PCB 36 mm X 18 mm X 1.5 mm; mounting pad for the collector lead min. 6 cm<sup>2</sup>.

## PNP General Purpose Amplifier

(continued)

### Electrical Characteristics

$T_A = 25^\circ\text{C}$  unless otherwise noted

| Symbol                     | Parameter                            | Test Conditions                               | Min | Max | Units |
|----------------------------|--------------------------------------|---|-----|-----|-------|
| <b>OFF CHARACTERISTICS</b> |                                      |   |     |     |       |
| $V_{(BR)CEO}$              | Collector-Emitter Breakdown Voltage* | $I_C = 1.0\text{ mA}, I_B = 0$                | 40  |     | V     |
| $V_{(BR)CBO}$              | Collector-Base Breakdown Voltage     | $I_C = 10\text{ }\mu\text{A}, I_E = 0$        | 40  |     | V     |
| $V_{(BR)EBO}$              | Emitter-Base Breakdown Voltage       | $I_E = 10\text{ }\mu\text{A}, I_C = 0$        | 5.0 |     | V     |
| $I_{BL}$                   | Base Cutoff Current                  | $V_{CE} = 30\text{ V}, V_{BE} = 3.0\text{ V}$ |     | 50  | nA    |
| $I_{CEX}$                  | Collector Cutoff Current             | $V_{CE} = 30\text{ V}, V_{BE} = 3.0\text{ V}$ |     | 50  | nA    |

### ON CHARACTERISTICS

|               |                                      |  |                             |              |        |
|---------------|--------------------------------------|--|-----------------------------|--------------|--------|
| $h_{FE}$      | DC Current Gain *                    | $I_C = 0.1\text{ mA}, V_{CE} = 1.0\text{ V}$<br>$I_C = 1.0\text{ mA}, V_{CE} = 1.0\text{ V}$<br>$I_C = 10\text{ mA}, V_{CE} = 1.0\text{ V}$<br>$I_C = 50\text{ mA}, V_{CE} = 1.0\text{ V}$<br>$I_C = 100\text{ mA}, V_{CE} = 1.0\text{ V}$ | 60<br>80<br>100<br>60<br>30 | 300          |        |
| $V_{CE(sat)}$ | Collector-Emitter Saturation Voltage | $I_C = 10\text{ mA}, I_B = 1.0\text{ mA}$<br>$I_C = 50\text{ mA}, I_B = 5.0\text{ mA}$   |                             | 0.25<br>0.4  | V<br>V |
| $V_{BE(sat)}$ | Base-Emitter Saturation Voltage      | $I_C = 10\text{ mA}, I_B = 1.0\text{ mA}$<br>$I_C = 50\text{ mA}, I_B = 5.0\text{ mA}$   | 0.65                        | 0.85<br>0.95 | V<br>V |

### SMALL SIGNAL CHARACTERISTICS

|           |                                  |  |     |      |     |
|-----------|----------------------------------|--|-----|------|-----|
| $f_T$     | Current Gain - Bandwidth Product | $I_C = 10\text{ mA}, V_{CE} = 20\text{ V},$<br>$f = 100\text{ MHz}$  | 250 |      | MHz |
| $C_{obo}$ | Output Capacitance               | $V_{CB} = 5.0\text{ V}, I_E = 0,$<br>$f = 100\text{ kHz}$  |     | 4.5  | pF  |
| $C_{ibo}$ | Input Capacitance                | $V_{EB} = 0.5\text{ V}, I_C = 0,$<br>$f = 100\text{ kHz}$  |     | 10.0 | pF  |
| NF        | Noise Figure                     | $I_C = 100\text{ }\mu\text{A}, V_{CE} = 5.0\text{ V},$<br>$R_S = 1.0\text{ k}\Omega, f = 10\text{ Hz to } 15.7\text{ kHz}$ |     | 4.0  | dB  |

### SWITCHING CHARACTERISTICS

|       |              |   |  |     |    |
|-------|--------------|---|--|-----|----|
| $t_d$ | Delay Time   | $V_{CC} = 3.0\text{ V}, V_{BE} = 0.5\text{ V},$ |  | 35  | ns |
| $t_r$ | Rise Time    | $I_C = 10\text{ mA}, I_{B1} = 1.0\text{ mA}$    |  | 35  | ns |
| $t_s$ | Storage Time | $V_{CC} = 3.0\text{ V}, I_C = 10\text{ mA}$     |  | 225 | ns |
| $t_f$ | Fall Time    | $I_{B1} = I_{B2} = 1.0\text{ mA}$               |  | 75  | ns |

\*Pulse Test: Pulse Width  $\leq 300\text{ }\mu\text{s}$ , Duty Cycle  $\leq 2.0\%$

**NOTE:** All voltages (V) and currents (A) are negative polarity for PNP transistors.

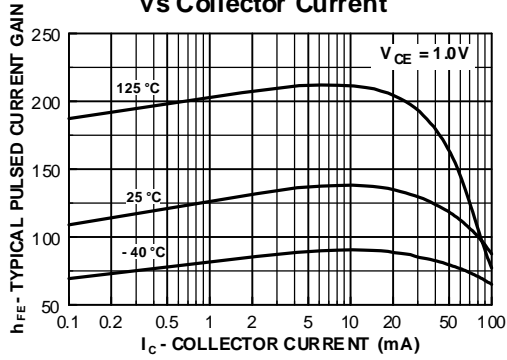
### Spice Model

PNP (Is=1.41f Xti=3 Eg=1.11 Vaf=18.7 Bf=180.7 Ne=1.5 Ise=0 Ikf=80m Xtb=1.5 Br=4.977 Nc=2 Isc=0 Ikr=0 Rc=2.5 Cjc=9.728p Mjc=.5776 Vjc=.75 Fc=.5 Cje=8.063p Mje=.3677 Vje=.75 Tr=33.42n Tf=179.3p Itf=.4 Vtf=4 Xtfc=6 Rb=10)

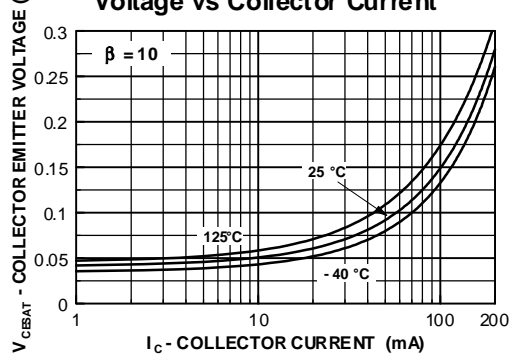
2N3906 / MMBT3906 / PZT3906

Typical Characteristics

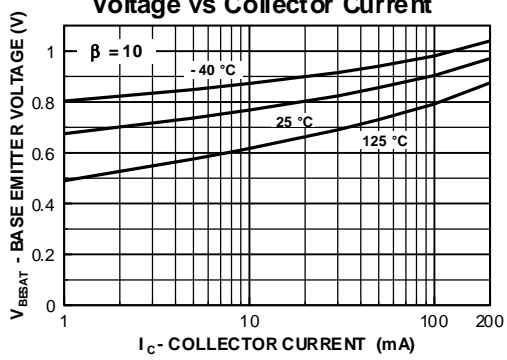
Typical Pulsed Current Gain vs Collector Current



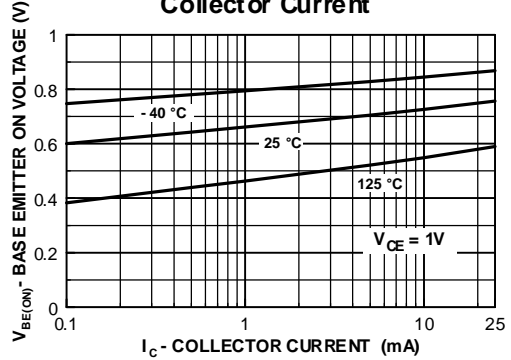
Collector-Emitter Saturation Voltage vs Collector Current



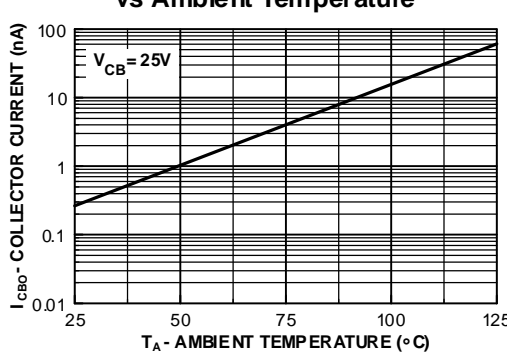
Base-Emitter Saturation Voltage vs Collector Current



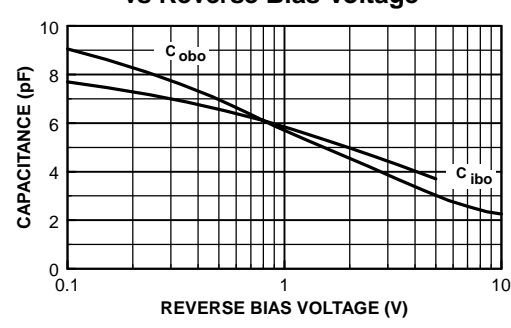
Base Emitter ON Voltage vs Collector Current



Collector-Cutoff Current vs Ambient Temperature



Common-Base Open Circuit Input and Output Capacitance vs Reverse Bias Voltage



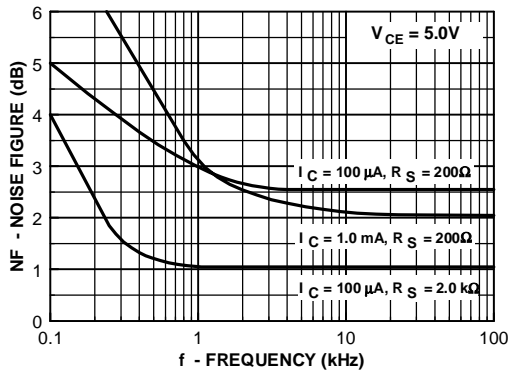
# PNP General Purpose Amplifier

(continued)

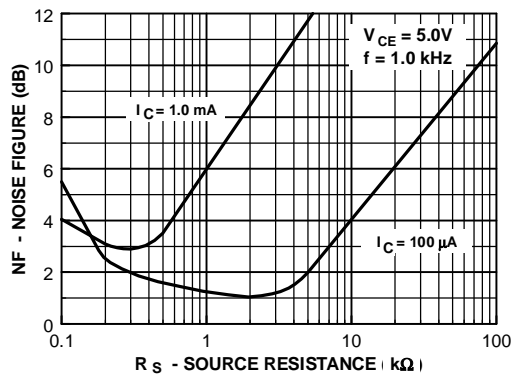
2N3906 / MMBT3906 / PZT3906

## Typical Characteristics (continued)

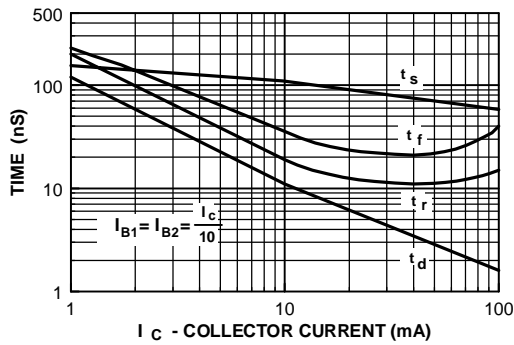
### Noise Figure vs Frequency



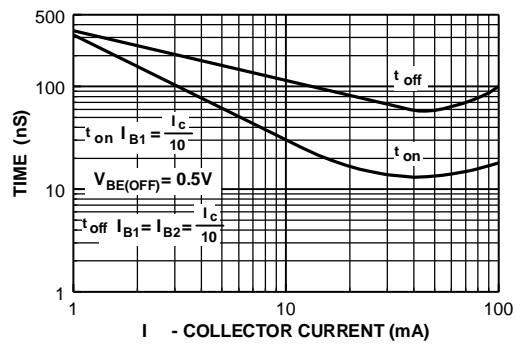
### Noise Figure vs Source Resistance



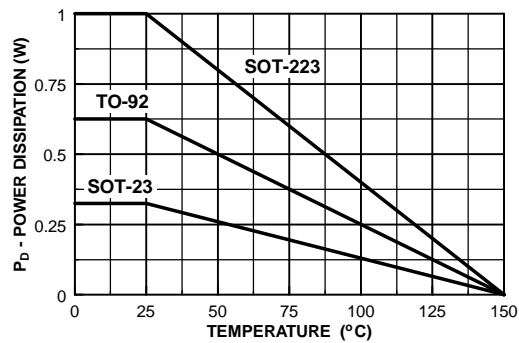
### Switching Times vs Collector Current



### Turn On and Turn Off Times vs Collector Current



### Power Dissipation vs Ambient Temperature

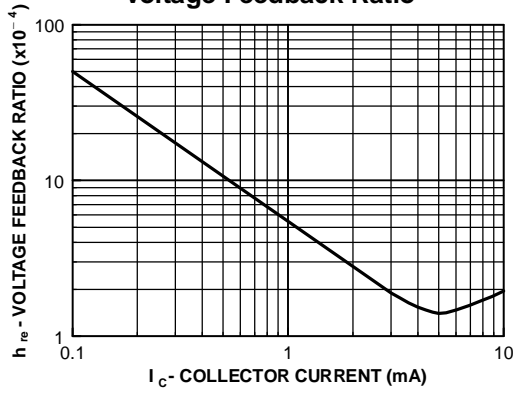


PNP General Purpose Amplifier  
(continued)

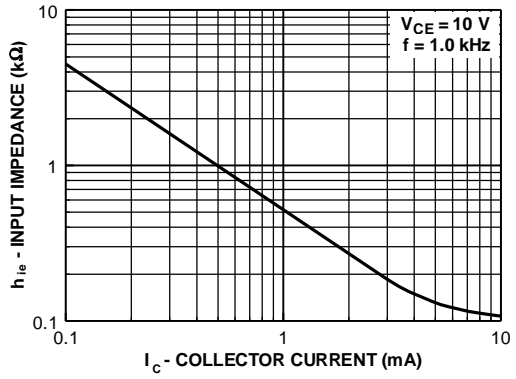
2N3906 / MMBT3906 / PZT3906

Typical Characteristics (continued)

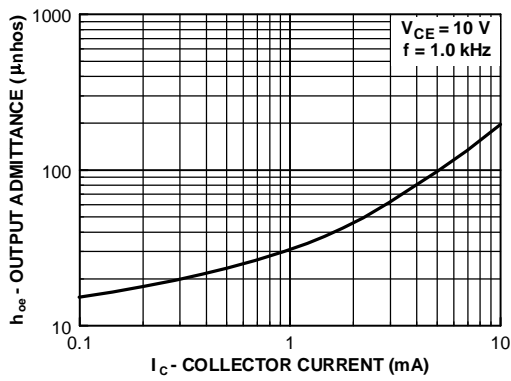
Voltage Feedback Ratio



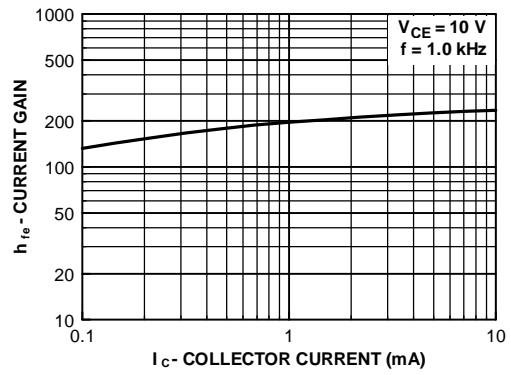
Input Impedance



Output Admittance



Current Gain



## TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

|                                   |                                  |                                  |                         |
|-----------------------------------|----------------------------------|----------------------------------|-------------------------|
| ACE <sup>x</sup> <sup>TM</sup>    | FAST <sup>r</sup> <sup>TM</sup>  | PowerTrench <sup>®</sup>         | SyncFET <sup>TM</sup>   |
| Bottomless <sup>TM</sup>          | GlobalOptoisolator <sup>TM</sup> | QFET <sup>TM</sup>               | TinyLogic <sup>TM</sup> |
| CoolFET <sup>TM</sup>             | GTO <sup>TM</sup>                | QS <sup>TM</sup>                 | UHC <sup>TM</sup>       |
| CROSSVOLT <sup>TM</sup>           | HiSeC <sup>TM</sup>              | QT Optoelectronics <sup>TM</sup> | VCX <sup>TM</sup>       |
| DOME <sup>TM</sup>                | ISOPLANAR <sup>TM</sup>          | Quiet Series <sup>TM</sup>       |                         |
| E <sup>2</sup> CMOS <sup>TM</sup> | MICROWIRE <sup>TM</sup>          | SILENT SWITCHER <sup>®</sup>     |                         |
| EnSigna <sup>TM</sup>             | OPTOLOGIC <sup>TM</sup>          | SMART START <sup>TM</sup>        |                         |
| FACT <sup>TM</sup>                | OPTOPLANAR <sup>TM</sup>         | SuperSOT <sup>TM</sup> -3        |                         |
| FACT Quiet Series <sup>TM</sup>   | PACMAN <sup>TM</sup>             | SuperSOT <sup>TM</sup> -6        |                         |
| FAST <sup>®</sup>                 | POP <sup>TM</sup>                | SuperSOT <sup>TM</sup> -8        |                         |

## DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

## LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

## PRODUCT STATUS DEFINITIONS

### Definition of Terms

| Datasheet Identification | Product Status         | Definition  |
|--------------------------|------------------------|---|
| Advance Information      | Formative or In Design | This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.  |
| Preliminary              | First Production       | This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design. |
| No Identification Needed | Full Production        | This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.   |
| Obsolete                 | Not In Production      | This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.   |